

V Semester B Tech Degree Examination, November 2015

Electrical & Electronics Engineering: Branch

2K6EE 507(P) Linear Integrated Circuits Lab

Centre: SNGCET, Payyannur

Date	Time	Reg. Nos.
27-1-16	9.00-12.00	B2ENEE3301,3304 B3ENEE3301,3303-3305
	10.30-1.30 pm	B3ENEE3306-3312
	12. 00-3.00 pm	B3ENEE3313-3319
28-1-16	9.00-12.00	B3ENEE3320-3325
	10.30-1.30 pm	B3ENEE3326-3331
	12. 00-3.00 pm	B3ENEE3323-3336 B1ENEE3301,3313, B2ENEE 3303

V Semester B Tech Degree Examination, November 2015

Electrical & Electronics Engg: Branch

2K6EE 508(P) Electrical Machines Lab –I

Centre: SNGCET, Payyannur

Date	Time	Reg. Nos.
27-1-16	9.00-12.00	B3ENEE 3320-3325
	10.30-1.30 pm	B2ENEE 3326-3331
	12. 00-3.00 pm	B3ENEE 3333 -3336 B1ENEE3303, B2ENEE3303
28-1-16	9.00-12.00	B3ENEE3301, 3303-3307
	10.30-1.30 pm	B3ENEE3308-3313
	12. 00-3.00 pm	B3ENEE3314-3319